

## **5. Integrated Circuits - Process Integration**

### **5.1 Basic Considerations for Process Integration**

#### **5.1.1 What is Integration?**

#### **5.1.2 Basic Concepts of Integrating Bipolar Transistors**

#### **5.1.3 Basic Concepts of Connecting Transistors**

#### **5.1.4 Integrated MOS Transistors**

#### **5.1.5 Integrated CMOS Technology**

#### **5.1.6 Summary to: 5.1 Basic Considerations for Process Integration**

### **5.2 Process Integration**

#### **5.2.1 Chips on Wafers**

#### **5.2.2 Packaging and Testing of Silicon Chips**

#### **5.2.3 Summary to: Chips on Wafers**

### **5.3 Money, Markets, and Management**

#### **5.3.1 Moore's Law and what it Means**

### **5.4 Summary**

#### **5.4.1 Summary to 5: Integrated Circuits - Process Integration**